EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	segment$5 with "serial register" with (multiplex$3 mux) same (memory buffer dram)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/15 14:33
L5	3877	(370/412,429).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2009/10/15 14:35
L6	195	"serial register" with (multiplex$3 mux)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/15 14:57
L7	0	L6 and L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/15 14:57
L8	0	((serial adj1 register) with segment$3 same (mux multiplex $3) and (memory buffer dram pbram $2ram)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/15 14:59
S1	2	(sram adj1 cell) with dram same "serial register" with (multiplex$3 mux)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:36
S2	2	dram same "serial register" with (multiplex$3 mux)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:44
S3	195	"serial register" with (multiplex$3 mux)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:45
S4	2	"serial register" with (multiplex$3 mux) same dram	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:45

S5	13	"serial register" with (multiplex$3 mux) same $2ram	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:46
S6	640	(buffer$3 stor$3 transfer$3) with (sram adj1 cell) same dram	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:50
S7	12	((buffer$3 stor$3 transfer$3) with (sram adj1 cell) same dram).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:50
S8	0	(buffer$3 stor$3 transfer$3) with (sram adj1 cell) same dram with (mux multiplex$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:51
S9	5	(buffer$3 stor$3 transfer$3) with (sram adj1 cell) same dram same (mux multiplex$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 01:53
S10	5	(buffer$3 stor$3 transfer$4) with (sram adj1 cell) same dram same (mux multiplex$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 02:06
S11	109	(sram adj1 cell) with (dram adj1 cell) same (mux multiplex $3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 02:07
S12	11	(sram adj1 cell) with (dram adj1 cell) with (mux multiplex$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2009/10/11 02:07

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	0	(segment$5 with "serial register" with (multiplex$3 mux) same (memory buffer dram)).clm.	USPAT; UPAD	OR	ON	2009/10/15 14:33

L3	0	(segment$5 with "serial register" same (multiplex$3 mux) same (memory buffer dram)).clm.	USPAT; UPAD	OR	ON	2009/10/15 14:34
L4	1	(segment$5 with "serial register" with (concurrent $3) same (memory buffer dram)).clm.	USPAT; UPAD	OR	ON	2009/10/15 14:34
L9	0	((serial adj1 register) with segment$3 same (mux multiplex$3) and (memory buffer dram pbram $2ram)).clm.	USPAT; UPAD	OR	ON	2009/10/15 15:00
L10	3	((serial adj1 register) with segment$3 same (mux multiplex$3) and (memory buffer dram pbram $2ram))	USPAT; UPAD	OR	ON	2009/10/15 15:00

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